

#### N-Channel Enhancement Mode Field Effect Transistor

# **General Description**

The 30N10 is a N-channel Power MOSFET. It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

### **Features**

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

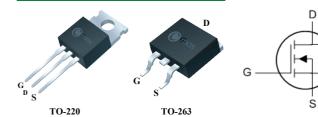
### **Product Summary**

BVDSS	RDSON	ID
100V	33mΩ	30A

### **Applications**

- LED power controller
- DC-DC & DC-AC converters
- High current, High speed switching
- Solenoid and relay drivers
- Motor control, Audio amplifiers

### TO-220/263 Pin Configuration



Туре	Package	Marking
CMP30N10	TO-220	CMP30N10
CMB30N10	TO-263	CMB30N10

# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	100	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current,VGS @ 10V	30	А	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current,VGS @ 10V	24	А	
I <sub>DM</sub>	Pulsed Drain Current	120	А	
EAS	Single Pulse Avalanche Energy	256	mJ	
P <sub>D</sub> @T <sub>C</sub> =25°C	Power Dissipation	145	W	
T <sub>STG</sub>	Storage Temperature Range -55 to 175		°C	
$T_J$	Operating Junction Temperature Range -55 to 175		°C	

# Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Junction-to-Ambient (PCB mount)		62	°C/W	
$R_{ heta JC}$	Junction-to-Case		0.97	°C/W	



### **N-Channel Enhancement Mode Field Effect Transistor**

# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	100			V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =20A			33	mΩ
T CDS(ON)	Static Dialii-Source Off-Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A			36	11122
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ =250uA	1		3	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =15A		23		S
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.0		Ω
Qg	Total Gate Charge	I <sub>D</sub> =27A		55		
$Q_gs$	Gate-Source Charge	V <sub>DD</sub> =50V		5		nC
$Q_{gd}$	Gate-Drain Charge	V <sub>GS</sub> = 10 V		14		
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =50V		11		
Tr	Rise Time	I <sub>D</sub> =27A		110		ns
$T_{d(off)}$	Turn-Off Delay Time	R <sub>G</sub> =4.7Ω		42		115
T <sub>f</sub>	Fall Time	V <sub>GS</sub> =10V		57		
C <sub>iss</sub>	Input Capacitance			1500		
Coss	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		115		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			100		

# **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			30	Α
I <sub>SM</sub>	Pulsed Source Current	V <sub>G</sub> -V <sub>D</sub> -UV , Force Current			120	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =20A , T <sub>J</sub> =25℃			1.2	V

Note:

This product has been designed and qualified for the counsumer market.

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