

#### **N-Channel Super Junction Power MOSFET**

## **General Description**

CMH65R115P is power MOSFET using Cmos's advanced super junction technology that can realize very low on-resistance and gate charge. It will provide much high efficiency by using optimized charge coupling technology.

These parts can be adopted quickly into new and existing offline power supply designs.

## **Features**

- Low On-Resistance
- 100% Avalanche Tested
- RoHS Compliant

### **Product Summary**

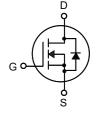
BVDSS	RDSON	ID
650V	115mΩ	33A

## **Applications**

- DC-DC Converters
- Adapter
- PFC Power Supply Stages
- Switching Applications

## **TO-247 Pin Configuration**





Туре	Package	Marking
CMH65R115P	TO-247	CMH65R115P

#### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	650	V	
$V_{GS}$	Gate-Source Voltage	±30	V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current	33	Α	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current	21	Α	
I <sub>DM</sub>	Pulsed Drain Current	132	Α	
EAS	Single Pulse Avalanche Energy <sup>1</sup>	211	mJ	
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	250	W	
T <sub>STG</sub>	Storage Temperature Range -55 to 150		$^{\circ}\! \mathbb{C}$	
TJ	Operating Junction Temperature Range -55 to 150		$^{\circ}$	

## **Thermal Data**

Symbol	Parameter	Rating	Unit	
$R_{\theta JA}$	Thermal Resistance Junction-ambient	62.5	°C/W	
R <sub>θJC</sub>	Thermal Resistance Junction-case	0.49	°C/W	



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## Electrical Characteristics (T<sub>J</sub>=25<sup>°</sup>C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	650			V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =15A		95	115	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D$ =250uA	2		4	٧
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =600V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±30V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =15A		24		S
Qg	Total Gate Charge	I <sub>D</sub> =33A		75		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =480V		15		nC
$Q_gd$	Gate-Drain Charge	V <sub>GS</sub> = 10V		34		
$T_{d(on)}$	Turn-On Delay Time	V <sub>DS</sub> =300V		50		
Tr	Rise Time	V <sub>BS</sub> = 300 V		105		no
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =33A		240		ns
T <sub>f</sub>	Fall Time	$R_G=25\Omega$		80		
C <sub>iss</sub>	Input Capacitance			2900		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		1800		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			110		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	$V_G$ = $V_D$ = $0V$ , Force Current			33	Α
I <sub>SM</sub>	Pulsed Source Current				132	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =15A			1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>DD</sub> =100V, I <sub>S</sub> =33A		488		ns
Qrr	Reverse Recovery Charge	dI <sub>F</sub> /dt =100A/μs		9.4		μС

#### Notes:

1. The EAS data shows Max. rating . The test condition is VDD=80V, VGS=10V, L=1mH, ID=6.5A

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