

### General Description

CMH65R115P is power MOSFET using Cmos's advanced super junction technology that can realize very low on-resistance and gate charge.

It will provide much high efficiency by using optimized charge coupling technology.

These parts can be adopted quickly into new and existing offline power supply designs.

### Features

- Low On-Resistance
- 100% Avalanche Tested
- RoHS Compliant

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	33	A
$I_D@T_C=100^\circ C$	Continuous Drain Current	21	A
$I_{DM}$	Pulsed Drain Current	132	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	211	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	250	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Data

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	62.5	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-case	0.49	$^\circ C/W$

### Product Summary

BVDSS	RDSON	ID
650V	115m $\Omega$	33A

### Applications

- DC-DC Converters
- Adapter
- PFC Power Supply Stages
- Switching Applications

### TO-247 Pin Configuration



Type	Package	Marking
CMH65R115P	TO-247	CMH65R115P

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$  , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	650	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=15A$	---	95	115	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	---	4	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=600V, V_{GS}=0V$	---	---	1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 30V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=15A$	---	24	---	S
$Q_g$	Total Gate Charge	$I_D=33A$	---	75	---	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=480V$	---	15	---	
$Q_{gd}$	Gate-Drain Charge	$V_{GS}=10V$	---	34	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=300V$ $V_{GS}=10V$ $I_D=33A$ $R_G=25\Omega$	---	50	---	ns
$T_r$	Rise Time		---	105	---	
$T_{d(off)}$	Turn-Off Delay Time		---	240	---	
$T_f$	Fall Time		---	80	---	
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1MHz$	---	2900	---	pF
$C_{oss}$	Output Capacitance		---	1800	---	
$C_{rss}$	Reverse Transfer Capacitance		---	110	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	33	A
$I_{SM}$	Pulsed Source Current		---	---	132	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=15A$	---	---	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{DD}=100V, I_S=33A$	---	488	---	ns
$Q_{rr}$	Reverse Recovery Charge	$di_F/dt=100A/\mu s$	---	9.4	---	$\mu C$

Notes:

1.The EAS data shows Max. rating . The test condition is  $V_{DD}=80V, V_{GS}=10V, L=1mH, I_D=6.5A$

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