

500V N-Channel MOSFET

General Description

The CMH20N50 uses advanced planar stripe DMOS technology and design to provide excellent RDS(ON).

These devices are well suited for high efficient switched mode power supplies and active power factor correction.

Features

- 100% avalanche tested
- Fast Switching
- Improved dv/dt capability
- RoHS Compliant

Product Summary

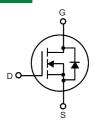
BVDSS	RDSON	ID
500V	0.26 Ω	20A

Applications

- Switching regulators
- UPS (Uninterruptible Power Supply)
- DC-DC converters

TO247 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	±30	V
I _D @T _C =25°C	Continuous Drain Current	20	А
I _D @T _C =100°C	Continuous Drain Current	12	Α
I _{DM}	Pulsed Drain Current ¹	60	А
EAS	Single Pulse Avalanche Energy ²	860	mJ
I _{AS}	Avalanche Current	20	А
P _D @T _C =25°C	Total Power Dissipation	280	W
T _{STG}	Storage Temperature Range -55 to 150		°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter		Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-ambient		40	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-case		0.44	°C/W



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Electrical Characteristics ($T_J=25$ °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	500			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃, I _D =250uA		0.5		V/℃
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =10A			0.26	Ω
V _{GS(th)}	Gate Threshold Voltage	V_{GS} = V_{DS} , I_D =250uA	2		4	V
	Durin Course Legland Course	V _{DS} =500V, V _{GS} =0V			1	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =400V , V _{GS} =0V , TC=125℃			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 30 V$, V_{DS} = $0 V$			±100	nA
gfs	Forward Transconductance 3	V _{DS} =40V , I _D =10A		24		S
Qg	Total Gate Charge	I _D =20A		52	69	
Q _{gs}	Gate-Source Charge	V _{DS} =400V		18		nC
Q _{gd}	Gate-Drain Charge	V _{GS} = 10V (Note 3, 4)		26		
T _{d(on)}	Turn-On Delay Time	V _{DS} =250V		88		
Tr	Rise Time	I _D =20A		270		20
T _{d(off)}	Turn-Off Delay Time	R _G =25Ω		105		ns
T _f	Fall Time	(Note 3, 4)		117		
C _{iss}	Input Capacitance			4000		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		380		pF
C _{rss}	Reverse Transfer Capacitance			35		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			20	Α
I _{SM}	Pulsed Source Current				60	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =20 A , T _J =25℃			1.4	V

Note

This product has been designed and qualified for the counsumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserver the right to improve product design ,functions and reliability wihtout notice.

^{1.}Repetitive Rating: Pulse width limited by maximum junction temperature

^{2.}L = 4.1mH, IAS = 20A, VDD = 50V, RG = 25Ω, Starting TJ = 25 $^{\circ}$ C

^{3.}Pulse Test: Pulse width≤300µs, Duty Cycle≤2%

^{4.} Essentially Independent of Operating Temperature Typical Characteristics